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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/410,974	10/01/1999	ANDREW M. JONES	99-TK-252	7705
7590 10/04/2004			EXAMINER	
LISA K JORGENSON			PHILPOTT, JUSTIN M	
STMICROELECTRONICS INC 1310 ELECTRONICS DR			ART UNIT	PAPER NUMBER
MAIL STOP 2346			2665	
CARROLLTON, TX 75000			DATE MAILED: 10/04/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Advisory Action	09/410,974	JONES ET AL.				
Advisory Action	Examiner	Art Unit				
	Justin M Philpott	2665				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address						
THE REPLY FILED 23 September 2004 FAILS TO PLA Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appearance (RCE) in compliance with 37 CFR 1.114.	void abandonment of this applic l) a timely filed amendment whi	cation. A proper reply to a ch places the application in				
PERIOD FOR RE	PLY [check either a) or b)]					
a) The period for reply expires 3 months from the mailing date of b) The period for reply expires on: (1) the mailing date of this Adv event, however, will the statutory period for reply expire later the ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).	isory Action, or (2) the date set forth in the an SIX MONTHS from the mailing date of FILED WITHIN TWO MONTHS OF THE	the final rejection. E FINAL REJECTION. See MPEP				
Extensions of time may be obtained under 37 CFR 1.136(a). The day have been filed is the date for purposes of determining the period of extens 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened (b) above, if checked. Any reply received by the Office later than three moleaned patent term adjustment. See 37 CFR 1.704(b).	sion and the corresponding amount of the statutory period for reply originally set in	fee. The appropriate extension fee under the final Office action; or (2) as set forth in				
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFI	•					
2. The proposed amendment(s) will not be entered be	ecause:					
(a) \( \square\) they raise new issues that would require further	er consideration and/or search (	see NOTE below);				
(b) they raise the issue of new matter (see Note by	pelow);					
(c) they are not deemed to place the application issues for appeal; and/or	in better form for appeal by mat	erially reducing or simplifying the				
(d) they present additional claims without cancel	ing a corresponding number of	finally rejected claims.				
NOTE:						
3. Applicant's reply has overcome the following rejection	tion(s):					
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a s	eparate, timely filed amendment				
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for reconsideration has been considered but does NOT place the application in condition for allowance because: <u>See Continuation Sheet</u> .						
6. The affidavit or exhibit will NOT be considered becaused by the Examiner in the final rejection.	cause it is not directed SOLELY	to issues which were newly				
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we	• • •	•				
The status of the claim(s) is (or will be) as follows:						
Claim(s) allowed:						
Claim(s) objected to:						
Claim(s) rejected:						
Claim(s) withdrawn from consideration:						
8. The drawing correction filed on is a) approved or b) disapproved by the Examiner.						
9. Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s)						
10. Other:		Wellfry				

HUY D. VU
SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2600 Part of Paper No. 20040924 Continuation of 5. does NOT place the application in condition for allowance because:

Applicant's arguments are not persuasive.

Specifically, applicant argues that the combination of Carson and Katzman do not teach the limitation "formed within an integrated circuit" as recited in applicant's claims 1, 6 and 7. However, this limitation is clearly discussed in the previous office action which states on page 4:

"Further, Carson teaches the invention is an improvement over a specific integrated circuit (e.g., Intel's 82C59A and also Intel's 82380, see col. 1, line 57 - col. 2, line 55) comprising functional modules (e.g., 11-14, see FIG. 1). Thus, Carson anticipates that the invention may similarly be exemplified by an integrated circuit comprising the functional modules by teaching that the invention is an improvement over the prior art integrated circuit comprising functional modules. Furthermore, it is generally considered to be within the ordinary skill in the art to shift the location of parts absent a showing of unexpected results. Thus, at the time of the invention it would have been obvious to one of ordinary skill in the art to shift the location of any elements from a location outside of a common integrated circuit since it is generally considered to be within the ordinary skill in the art to shift the location of parts absent a showing of unexpected results. The contention of obvious choice in design can be overcome if Applicant establishes unexpected results. In re Japikse, 86 USPQ 70 (CCPA 1950). Thus, at the time of the invention it would have been obvious to one of ordinary skill in the art to include the elements taught by Carson within a common integrated circuit since Carson teaches that the invention is an improvement over the prior art integrated circuit comprising functional modules, and since it is generally considered to be within the ordinary skill in the art to shift the location of parts absent a showing of unexpected results."

More specifically, applicant then argues that "the Intel 82C59A and/or 82480 were marketed as members of 'chip sets', not stand-alone integrated circuits" (page 8). However, such an argument is moot for a number of reasons. First, applicant's claims clearly recite "[a]n integrated circuit comprising: ..." (emphasis added). That is, applicant has not claimed the recited functional modules of applicant's invention are contained in a stand-alone integrated circuit. Rather, by reciting "comprising", applicant has claimed an integrated circuit having the functional modules as well as possibly any other components common to an integrated circuit. Second, applicant has asserted particular marketing of a specific Intel product without providing any evidence to support such an assertion. Therefore, the alleged marketing of Intel cannot be reasonably considered by Examiner. Third, in the event applicant were to provide evidence regarding the marketing of these specific Intel products, such evidence cannot negate the clear teachings of the cited prior art. That is, Carson teaches an improvement over an existing integrated circuit, wherein the improvement comprises the functional modules as recited in applicant's claims. Accordingly, applicant's argument is not persuasive.

Still further, applicant argues that Examiner's statement, "it is generally considered to be within ordinary skill in the art to shift the location of parts absent a showing of unexpected results", is not supported by reference to authority. However, as recited in the previous office action, and repeated herein, authority is clearly provided by In re Japikse, 86 USPQ 70 (CCPA 1950). Furthermore, this argument regarding obviousness to shift the location of parts is an additional reason that the combination of Carson and Katzman teach all of the elements of applicant's claims. Both the teachings of Carson discussed above, and the authority provided by In re Japikse are clear indications that the combination of Carson and Katzman teach all of the elements of applicant's claims. Accordingly, applicant's argument is not persuasive.